Sneet	1	of 1	£ ;	~j.		OIP	E JC.						
INFORMATION DISCLOSURE CITATION			550-243 (or 1 1 2001)		1 2001 2	SERIAL NO. 09/887,560							
(Use several sheets if necessary)			NEVILL et al. FILING DATE		HADEMARK	G	GROUP						
				June	25, 200	1							
				ί, ι	J.S. PAT	ENT DOC	UMENTS					-	
*EXAMINER INITIAL		CUMENT I	MRED	DATE			NAME		(CLASS	SUBCLASS	FILING	DATE
B50	TT	5,790,8		8/1998	I		Traut		<u> </u>	LAGO	SUBULAGO	TIE AFER	OFFIAIL
	 	5,898,8		4/1999	-	,	kol et al						
<u>B50</u>	 	5,898,8		4/1999			kol et al						
<u>BX</u>	\square												
<u>B30</u>	 	5,953,5	520	9/1999		IV	1allick					<u> </u>	
	 				ļ							<u> </u>	
												1	
												-	
	 				ļ							ļ	
							-						
				-			W	•			· · · · ·		
	 												
	 											<u>† </u>	
					 		<u> </u>					<u> </u>	
	 				-				-			1 -	
				E/	DEIGN	DATERIT	DOCUMEN	TC					
					THEIGH	PAICINI	DOCUMEN					TRANS	LATION
		DOCUM		DATE			DUNTRY		(CLASS	SUBCLASS		NO
BSO		WO 00/3	4844	6/2000		\	VIPO		•=			•	
		0											
·								188					
					<u> </u>							1	
		ОТН	IER DOC	UMENTS	(includin	g Author	, Title, Date	e, Perti	nent p	ages,	etc.)		
	Π						· · ·	-					
*Examiner			~~~~~ <u>~</u>	Date Consid			dered		6/2	5/04			
Examiner: Initia	al if referen	nce considere	ed, whether of	not citation is i	n conforman	ce with MPEP	609; Draw line	through cit	tation if no	t in confo	ormance and no	t considered	. Include
copy of this forn	n with next	t communica	tion to applica	ation.					· ·		Form PTO-FB		

*Examiner Date Considered

8/2003

Examiner: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to application.

oritor z o		Atty. Docket N	lo.	erial No.				
•		550-243	550-243 09/887,560					
INFOR	MATION DISCLOSURE		Applicant 05/067,300					
	CITATION					-11/15	D	
(1100	several sheets (necessary)	NEVILL			RECE		<u>.</u>	
(USE	several sheets/(necessary)	Filing Date		Group	. 20		t.	
	2004	ළ June 25, 2	2001		APR 0	1 200	4	
	MAR 3 1 2004	<u>June 25, 2</u>			Toohnolomi	Contor	2100	
		L. /	PATENT DOCUMENTS		Technology	Centai	2100	
	TRADEMA	<u> </u>				TRANS	LATION	
	DOCUMENT	DATE	COUNTRY	CLASS	SUBCLASS	YES	NO	
	<u> </u>	- I	00011111		T	163		
					++		-	
	OTUED DOOL	MENTO (<u> </u>		<u> </u>	
923			g Author, Title, Date, Po		etc.)			
B30	H. Stone, Chapter 12 - "A Pipeline Push-Down Stack Computer", 1969, pages 235-249 C. Glossner et al, "Delft-Java Link Translation Buffer", 8/1998							
_				- H 1000				
			chitectural Support For a Jav	va Processor" 1998	, pages 330-35:	5		
_			An Introduction", 8/1997					
-			ed On Fast Dynamic Compi	lation", 1/1997, pa	ges 1-13			
	A. Wolfe, "First Java-spe		<u> </u>	T.D. 1.4000				
			From Bits and Gates to C an	d Beyond, 1999, pa	ges 1-517			
	M. Ertl, "Stack Caching							
	M. Ertl, "Stack Caching							
	M. Ertl, "Implementation	of Stack-Based La	nguages on Register Machin	nes" 4/1996, pages	1-4			
	J. O'Connor et al, "PicoJ	ava-I: The Java Virt	ual Machine in Hardware"	IEEE Micro A Cas	e for Intelligen	t RAM,	r	
	March/April 1997, pages							
	K. Andrews et al, "Migra	ating a CISC Compu	iter Family Onto RISC Via	Object Code Trans	lation" 1992, p	ages 21	3-222	
			re" 10/1996, pages 1-8, Sun	Microsystems				
	M. Ertl, "A New Approa							
	M. Maierhofer et al, "Op							
			lltalk on a RISC" The 11th A	Annual Internationa	l Symposium o	on Comp	puter	
J	Architecture, 6/1984, pag							
			pret Virtual Machine Instru		ges 1-59			
	R. Kapoor et al, "Stack R	Renaming of the Java	a Virtual Machine" 12/1996	, pages 1-17				
1	A. Yonezawa et al, "Imp	lementing Concurre	nt Object-Oriented Languag	ges in Multicomput	ers" <i>Parallel a</i>	nd Distr	ributed	
	Technology (Systems and	l Applications) 5/19	93, pages 49-61				_	
	C. Hsieh et al, "Java Byte	ecode to Native Cod	le Translation; The Caffeine	Prototype and Pre	liminary Resuli	ts" IEEI	E/ACM	
	International Symposium			·				
	Y. Patt et al, Introduction	to Computer System	ms From Bits and Gates to	C and Beyond, 200	1, pages 1-526			
ļ	Sun Microsystems PicoJava Processor Core Data Sheet, 12/1997, pages 1-11							
ļ	H. McGhan et al, PicoJava A Direct Execution Engine for Java Bytecode, 10/1998, pages 22-26							
<u> </u>	C. Glossner et al, "Parallel Processing" Euro-Par 1997: Passau, Germany, 8/1997							
	Y. Patt, Introduction to Computer Systems From Bits and Gates to C and Beyond, 1999, pages 10-12 & 79-82							
	Espresso - The High Performance Java Core Specification, 10/2001, pages 1-33, Aurora VLSI, Inc.							
	J. Gosling, "Java Intermediate Bytecodes" 1995, pages 111-118							
	P. Koopman, Jr. "Stack Computers The New Wave" 1989, pages 1-234							
\	M. Mrva et al, "A Scalable Architecture for Multi-Threaded JAVA Applications" Design Automation and Test in Europe							
ļ	2/1998, pages 868-874						-	
	L. Chang et al, "Stack Op	erations Folding in	Java Processors" IEEE Proc	c Comput. Digit.	Tech., Vol. 14:	5, No. 5	, pages	
1	333-340 9/1998							
1	L. Ton et al, Proceedings	of the '97 Internation	onal Conference on Parallel	and Distributed Sy	stems, "Instruc	tion Fol	ding in	
V	Java Processor", pages 13	38-143, 12/1997						
				, / -	1000			
Examiner	1 700		Date Considere	d <i>UIS</i> S	104			

Examiner: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to application.

Sheet 3 o	Atty. Docket No.	erial No.					
INFOR	MATION DISCLOSURE CO. Applicant	09/887,560					
	several sheets if necessary \ 2004 Several sheets i	Group					
B30	K. Buchenrieder et al, "Scalable Processor Architecture for 33, No. 18, pages 1532+, 8/1997	Java With Explicit Thread Support" Electronics Letters Vol.					
	C. Chung et al, Proceedings of the '98 International Confer Java Processor for Java Multithreading" pages 693-700, 12	ence on Parallel and Distributed Systems, "A Dual Threaded /1998					
	I. Kazi et al, "Techniques for Obtaining High Performance in Java Programs" 9/2000, pages 213-240						
	R. Kieburtz, "A RISC Architecture for Symbolic Computation" 1987, pages 146-155						
	M. Berekovic et al, "Hardware Realization of a Java Virtual Machine for High Performance Multimedia Applications" Signal Processing Systems SIPS 98, pages 479-488, 1997						
	P. Deutsch, "Efficient Implementation of the Smalltalk-80 System" 1983, pages 297-302						
	"Rockwell Produces Java Chip" 9/1997, CNET NEWS.COM						
V	Y. Patt et al, Introduction to Computing Systems from Bits 195-209	and Gates to C and Beyond, 2001, pages 1-16, 91-118 &					

RECEIVED

APR 0 1 2004

Technology Center 2100

*Examiner Date Considered 6/25/04